



tech hubs | OFFICIAL AWARDEE
NY SMART
I - CORRIDOR

Greater Rochester Quality Council
26 February 2025

Brief History

May 2020:
Endless
Frontier Act
introduced

Nov 2021:
BUF-ROC
leadership
exchange #1

Aug 2022:
CHIPS &
Science
Act signed
into law

Dec 2022:
Initial \$500M
appropriated
for Regional
Tech Hubs

July 2024:
NY SMART I-Corridor
awarded a first-in-the-
nation implementation
award

April 2021:
US Innovation
& Competition
Act introduced

June 2022:
BUF-ROC
leadership
exchange #2

Oct 2022:
Micron
announces
intent to build
in CNY

Oct 2023:
NY SMART I-Corridor
selected as one of 31
tech hubs

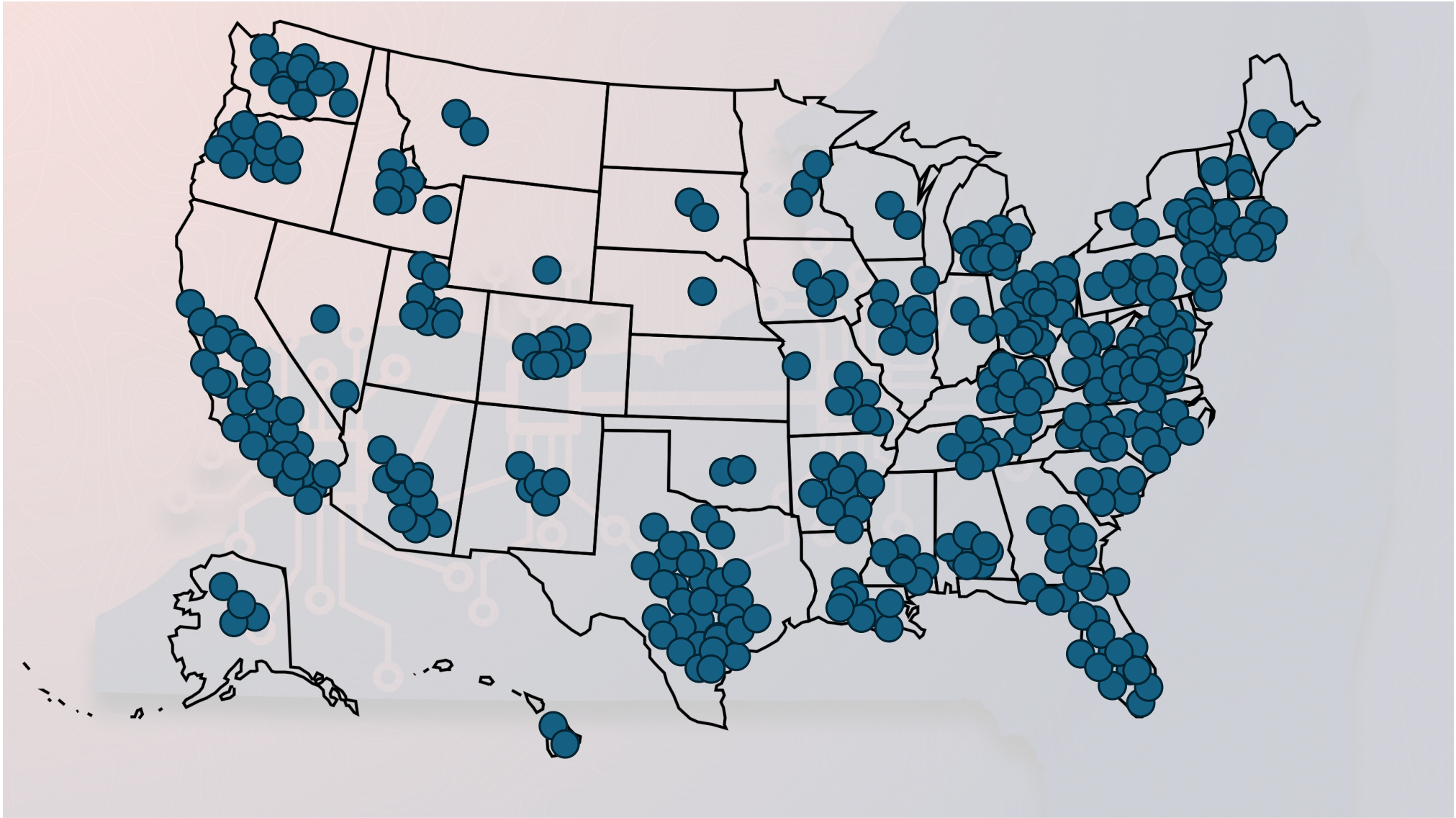


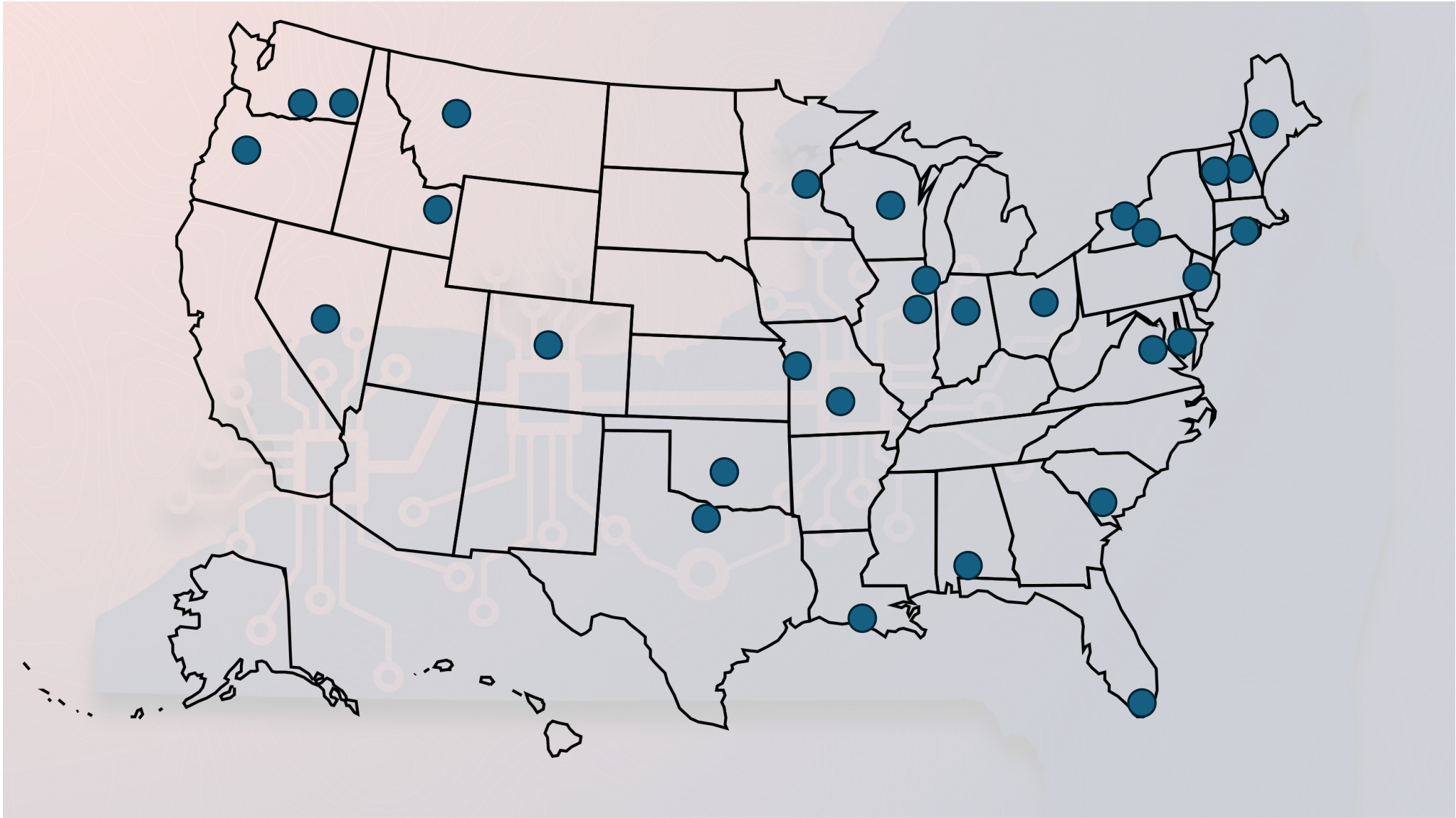
CHIPS and Science Act (2022)

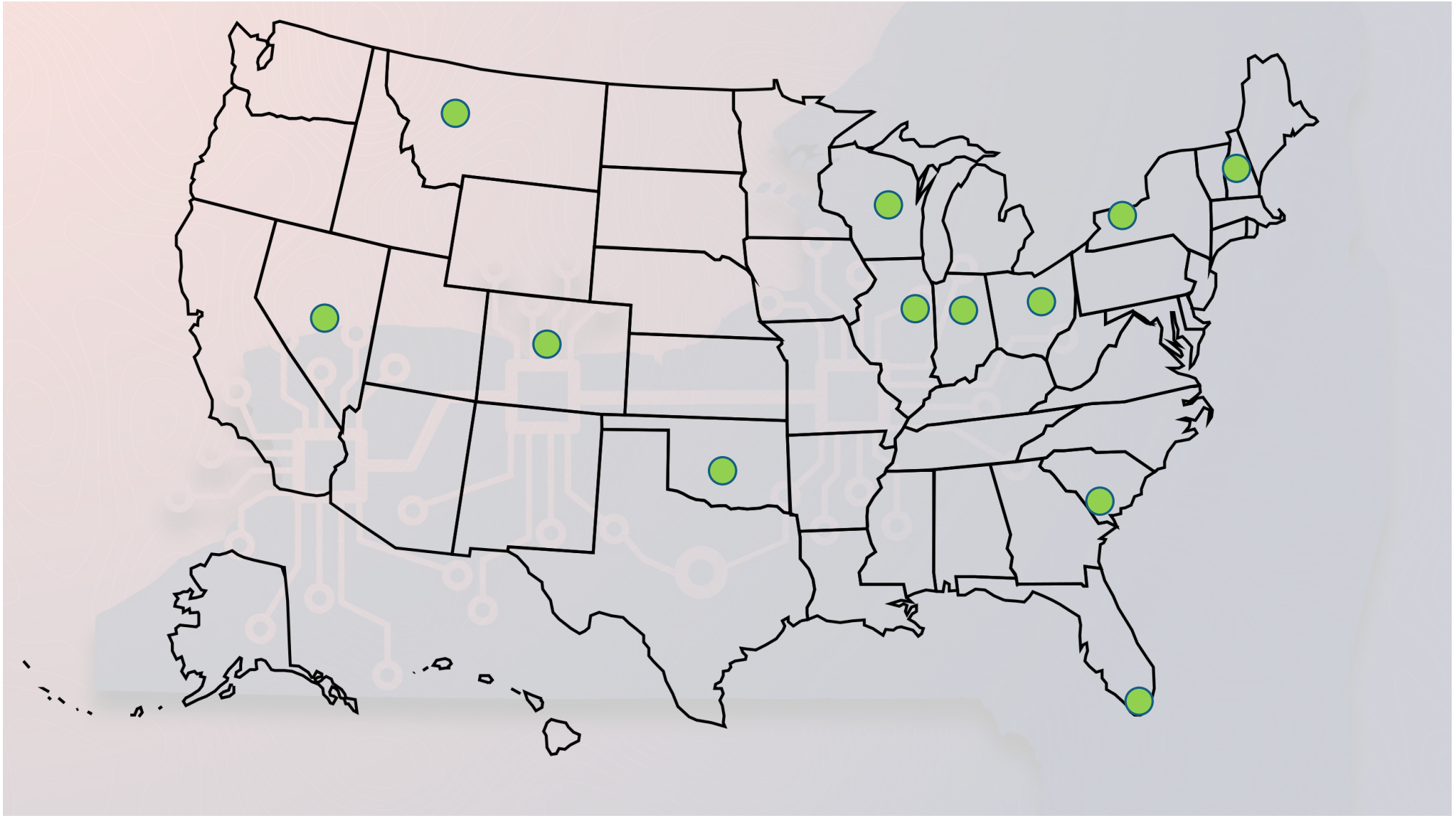
Bolster U.S. leadership in semiconductors through \$50bn in manufacturing incentives and workforce

Advance U.S. leadership in critical technologies of the future through investment in regional hubs









A stylized map of New York State is shown against a background of topographic contour lines. The map is overlaid with a network of light-colored circuit traces and nodes. Three specific locations are highlighted with white rectangular boxes containing black text: Buffalo, Rochester, and Syracuse. The Buffalo box is on the left, Rochester is in the center, and Syracuse is on the right. The background has a color gradient from light orange on the left to dark blue on the right.

Buffalo

Rochester

Syracuse



Buffalo

Rochester

Syracuse

130 miles

Buffalo

Rochester

MENLO

MICRON

Syracuse

WOLFSPEED

GLOBAL



EDWARDS

Rochester

MICRON

TTM

Syracuse

WOLFSPEED

Buffalo

AMD

OPTIMAX

OnSEMI

GLOBAL

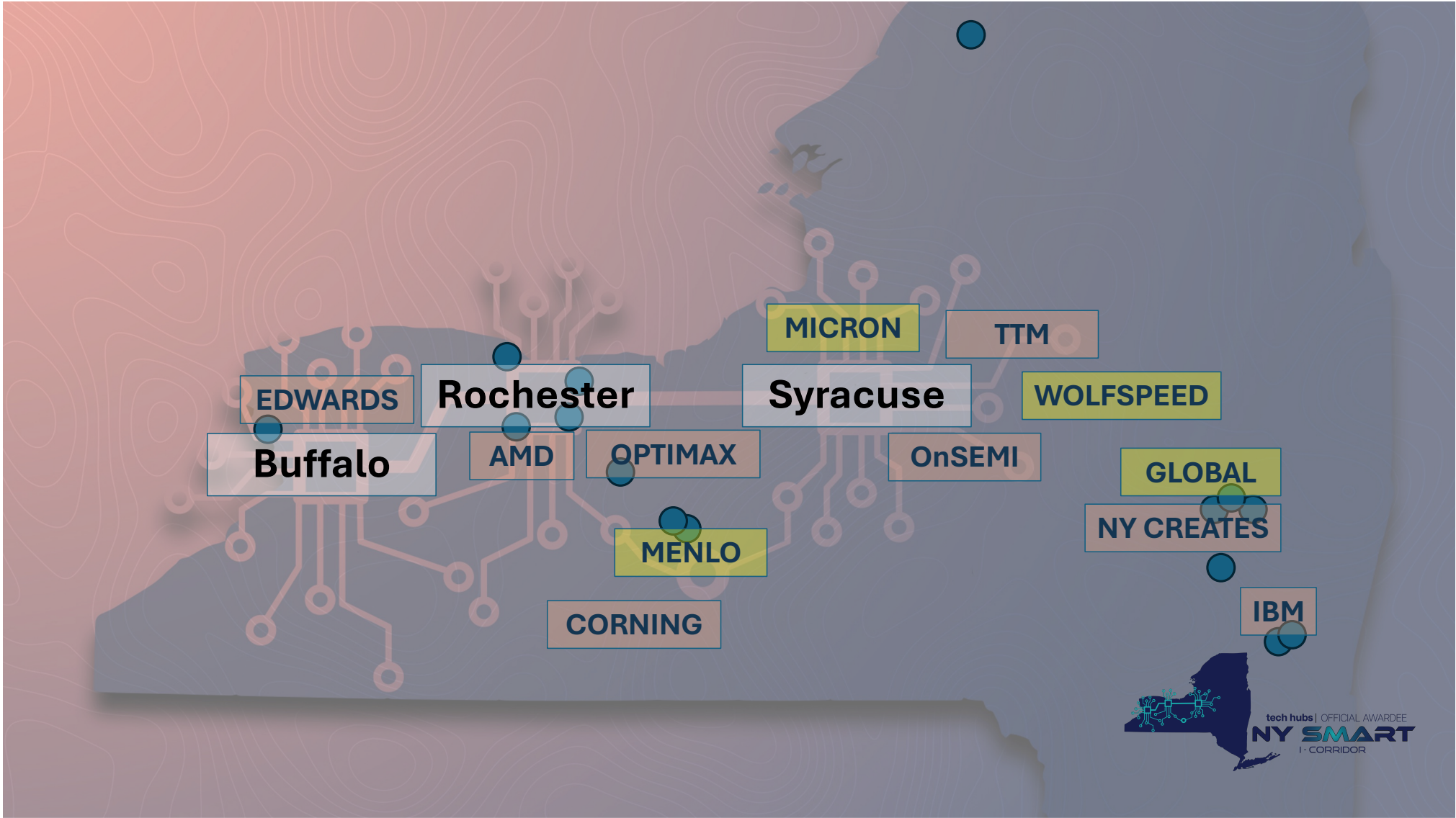
MENLO

NY CREATES

CORNING

IBM





NY SMART I-Corridor Tech Hub

- Spanning the **Buffalo-Rochester-Syracuse super-region**
- Leveraging a **robust statewide semi ecosystem**
- Focused on the **full semiconductor value chain**
- Key assets in **optics, photonics, adv manufacturing, microelectronics**
- Geography facing a **transformational demand driver**



A Transformational Demand Driver

Based on planned investments alone,
by 2033 **one-in-four** domestically-produced
semiconductor chips will be manufactured within
350 miles of the NY SMART I-Corridor



NY SMART I-Corridor Tech Hub



Preliminary illustration, subject to change.



The Consortium: By the Numbers

- Four MSAs + two MiSAs
- 100+ organizational members
- Five R-1 and R-2 research universities
- Three co-convening organizations
- Four component projects
- Thirteen subawardee implementation partners
- \$40MM EDA + \$10MM NYS + \$20MM consortium commitments ++



Governance Structure

ONE >>
ROC

BNP BUFFALO
NIAGARA
PARTNERSHIP

CEO CENTERSTATE
CORPORATION FOR
ECONOMIC OPPORTUNITY

Co-Convener
Leadership Group



Advisory Board



Project Leadership
Teams



Component Projects

Supply Chain

Investment in supply chain activation to pivot incumbent advanced manufacturers and attract new firms

Commercialization

Investment in commercializing tech with in-region firms and expanding access to innovation and prototyping assets

Workforce

Investment in workforce development to expedite the training of thousands of new technicians and in-demand jobs

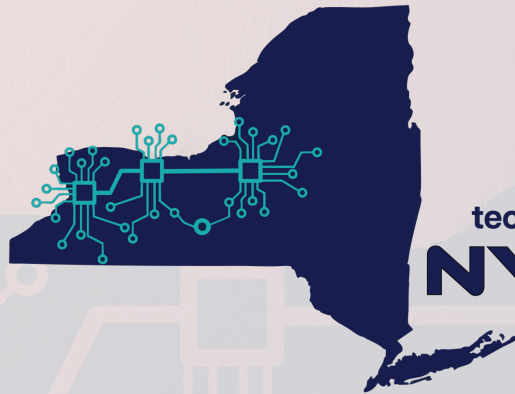
Governance

Investment in regional coordination to ensure integrated governance and expansion of the corridor-wide ecosystem



Goals

Supply Chain	Commercialization	Workforce	Governance
2x the number of semiconductor supply chain firms in the corridor through pivot-enabling technical assistance and capital investment	~550 patents per year for high performance computing with ~100 patent licensees in the semiconductor space, and 150+ firms accessing innovation vouchers	Bring online 5,000+ newly trained individuals in middle-skill semiconductor roles, and 3,200+ in construction roles	Formalize super-regional consortium, foster consistent implementation across markets, ensure \$\$ sustainability and inform strategy evolution



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